

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



INTERNATIONAL BUREAU OF PATENT COOPERATION
3, rue de la Harpe, CH-1001 Nyon, Switzerland
P.O. Box 6859, CH-1001 Nyon, Switzerland
Tel. (41) 26 30 65 11, Fax (41) 26 30 65 12
E-mail: wipoint@wipo.int, wipomail@wipo.int

(43) International Publication Date
21 June 2001 (21.06.2001)

PCT

(10) International Publication Number
WO 01/44865 A1

(51) International Patent Classification⁷: G02F 1/1339

(21) International Application Number: PCT/SG99/00145

(22) International Filing Date:
17 December 1999 (17.12.1999)

(25) Filing Language: English

(26) Publication Language: English

(71) Applicant (for all designated States except US): INFI-
NEON TECHNOLOGIES ASIA PACIFIC PTE. LTD.
[SG/SG]; 168 Kallang Way, Singapore 349 (SG).

(72) Inventor; and

(75) Inventor/Applicant (for US only): GUENTHER, Ewald,
Karl, Michael [DE/SG]; 991 Bukit Timah Road, #03-11
Maplewoods, Singapore 589630 (SG).

(74) Agent: SIEMENS ADVANCED ENGINEERING PTE.
LTD.; Intellectual Property Dept., 166 Kallang Way, Sin-
gapore 349249 (SG).

(81) Designated States (national): AE, AL, AM, AT, AU, AZ,
BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE,
ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP,
KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD,
MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD,
SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ,
VN, YU, ZA, ZW.

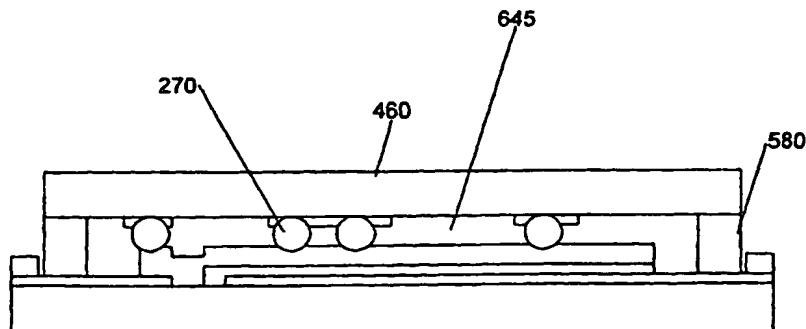
(84) Designated States (regional): ARIPO patent (GH, GM,
KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent
(AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent
(AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU,
MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

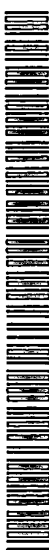
— With international search report.

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: IMPROVED ENCAPSULATION FOR ORGANIC LED DEVICE



(57) Abstract: An encapsulation for a device is disclosed. In one embodiment, spacer particles (270) are selectively located in the device region. The spacer particles (270) prevent a cap mounted on the substrate (460) from contacting the active components, thereby protecting them from being damaged.



WO 01/44865 A1

IMPROVED ENCAPSULATION FOR ORGANIC LED DEVICE

Field of the Invention

The present invention relates to organic LED (OLED) devices. More particularly, the invention relates to packaging of OLED devices.

Background of the Invention

Fig. 1 shows an OLED device 100. The OLED device comprises one or more organic functional layers 110 between first and second electrodes 105 and 115. The electrodes can be patterned to form, for example, a plurality of OLED cells to create a pixelated OLED device. Bond pads 150, which are coupled to the first and second electrodes, are provided to enable electrical connections to the OLED cells.

To protect the OLED cells from the environment such as moisture and/or air, a cap 160 encapsulates the device. The active and electrode materials of the OLED cells are sensitive and can be easily damaged due to mechanical contact with, for example, the cap. To prevent damage to the OLED cells, a cavity cap or package is used. The cavity package provides a cavity 145 between the cap and OLED cells. The cavity also

allows for the placement of desiccant materials to cope with finite leakage rate of the device.

The lateral dimensions of OLED devices are usually in the range of a few centimeters or more, depending on
5 the applications. To accommodate the relatively large lateral dimensions, thicker caps are used to provide the necessary mechanical stability to maintain the integrity of the cavity. However, the demand for thin and flexible devices requires the use of thinner components,
10 such as the cap and the substrate. Decreasing the thickness of the cap reduces its mechanical stability, making it more prone to bending which can cause the cavity to collapse, thereby damaging the OLED cells.

As evidenced from the above discussion, it is
15 desirable to provide an OLED device having improved packaging, particularly those formed on thin or flexible substrates.

Summary of the Invention

20 The invention relates to improved encapsulation for devices. In one embodiment, an improved encapsulation is provided for an OLED device. The OLED device includes OLED cells formed in the device region of a substrate. Spacer particles are selectively located in

the device region to provide support for a cap mounted on the substrate. The spacer particles are selectively located, for example, in the inactive or non-emissive regions between the OLED cells. The use of spacer
5 particles prevents the cap from contacting the OLED cells in the cell region due to stress induced by bending, improving reliability.

10 **Brief Description of the Drawings**

Fig. 1 shows an OLED device;

Figs. 2-6 show a process for forming an OLED device in accordance with one embodiment of the invention;

Figs. 7-8 show a process for forming an OLED device
15 in accordance with another embodiment of the invention;

Figs. 9-13 show a process for forming an OLED device in accordance with another embodiment of the invention; and

Fig. 14 shows a process for forming an OLED device
20 in accordance with another embodiment of the invention.

Preferred Embodiments of the Invention

The invention relates generally to OLED devices. In particular, the invention provides a cost-effective
25 package for encapsulating OLED devices, particularly

those formed on thin substrates. In accordance with the invention, spacer particles are located in the device or cell region of the device. The spacer particles support the cap, preventing it from contacting the OLED cells.

5 The use of spacer particles in OLEDs is described in concurrently filed international patent application titled "Encapsulation For Organic LED Device" (attorney docket number GR 99E 2809SG), which is herein incorporated by reference for all purposes.

10 The spacer particles are spatially distributed on, for example, a substrate with the desired pattern. In one embodiment, the spacer particles are spatially located in the non-emissive or non-active areas of the device region in which OLED cells are formed. The
15 spacer particle pattern distribution can be formed from a direct or indirect patterning process.

Figs. 2-6 show a process for fabricating an OLED device in accordance with one embodiment of the invention. Referring to Fig. 2, a substrate 260 is
20 provided. The substrate, in one embodiment, serves as a cap for encapsulating the OLED device. The cap comprises, for example, metal or glass. Other types of materials which can protect the active components from the environment, such as ceramic or metallized foil, are

also useful. The thickness of the cap can be, for example, 10 - 200 μm .

In accordance with the invention, spacer particles 270 are randomly distributed on the substrate surface 261. The spacer particles are randomly deposited on the substrate surface by depositing a device layer having spacer particles suspended therein. In one embodiment, the device layer comprises a photosensitive material, such as photoresist. Various types of photoresists can be employed, for example, AZ 5214E from Clariant or TSMR 8900 from Tokyo Oka. Other photosensitive materials in which spacer particles can be suspended, such as photopatternable polyimide, photopatternable polybenzoxazole, photopatternable polyglutarimide and other resins, are also useful. Suspending spacer particles in a photosensitive material advantageously enables the formation of the spacer particle distribution on the substrate by a direct patterning process.

The shape of the spacer particles can be spherical with a diameter substantially equal to about the desired height of the cavity created by the cap. The cavity for OLED devices typically is about 2 - 500 μm . Spacer particles having other geometric shapes, such as

cubical, prism, pyramidal, or other regular or irregular shapes, with an average mean diameter sufficient to maintain the desired height of the cavity can also be used. The size distribution of the spacer particles
5 should be sufficiently narrow to ensure proper separation between the cap and OLED cells.

The spacer particles preferably comprise a non-conductive material. In one embodiment, the spacer particles are made of glass. Spacer particles made of
10 other types of non-conductive (e.g., silica, polymers, or ceramic) or conductive materials are also useful. The use of spacer particles comprising conductive materials is also useful.

The concentration of spacer particles in the
15 photoresist should be sufficient to produce a desired spacer particle density in the final pattern distribution. In one embodiment, the concentration of spacer particles should be sufficient to produce a spacer particle density in the pattern distribution to
20 prevent the cap from contacting the OLED cells. In one embodiment, the photoresist comprises a spacer particle concentration of about 0.1-2% (by weight). Such a concentration produces a spacer particle density in which the average distance between the spacer particles

is about 10 - 500 μm . Typically, this is sufficient in preventing the cap from contacting the OLED cells. The concentration of spacer particles can be varied to accommodate design requirements, such as the thickness of the 2 caps, thickness of the substrate and amount of device flexibility needed.

The photoresist is deposited (e.g., spin-on techniques) on the substrate surface, forming a resist layer 266 with spacer particles 270 embedded therein.

10 The thickness of the resist layer should be less than the diameter of the spacer particles. Typically, the resist layer is about 100-2000 nm thick.

Referring to Fig. 3, radiation 398 from an appropriate exposure source (ultra-violet, deep ultra-violet, x-ray electron beam, or ion beam, depending on the resolution and resist) passes through a mask 395 to selectively expose regions 397 of the resist layer 260.

In Fig. 4, the resist is appropriately developed (e.g., wet chemicals) to remove the exposed portions.

20 Both the resist and spacer particles in the exposed portions are removed, leaving the desired spacer particle distribution pattern on the surface of the cap. As described, the resist is a positive resist. The use of negative resist is also useful.

In an alternative embodiment, the spacer particle distribution pattern is created using an indirect patterning process. The device layer in which the spacer particles are suspended comprises a non-

5 photopatternable material, such as novolac solution, benzocyclobutene solution, polyimidesolution, polybenzoxazole solution, polyglutarimide solution and other acrylic or epoxy resins. Other non-

10 photopatternable materials in which the spacer particles can be suspended is also useful. The device layer is deposited on the substrate by, for example, spin-coating or other wet coating processes such as doctor blading. Wet printing techniques can also be used to deposit the spacer particles on the substrate surface. A resist

15 layer is deposited over the spacer particles and patterned to remove portions of the resist. The remaining resist serves as an etch mask, for example, a wet etch process, to remove unwanted spacer particles and portions of the device layer from the cap surface.

20 The use of other types of photopatternable materials to form the etch mask is also useful.

Referring to Fig. 5, a thin substrate 501 is prepared with active components. The substrate comprises, for example, a plastic film such as, for

example, transparent poly(ethylene terephthalate) (PET), poly(butylene terephthalate) (PBT), poly(ethylene naphthalate) (PEN), Polycarbonate (PC), Polyimides (PI), Polysulfones (PSO), and poly(*p*-phenylene ether sulfone) (PES). Other materials such as Polyethylene (PE), Polypropylene (PP), poly(vinyl chloride) (PVC), Polystyrene (PS) and Poly(methyl methacrylate) (PMMA), can also be used to form the substrate. A substrate comprising glass or other transparent materials such as quartz is also useful. Other materials can also be used to form the substrate, depending on the application.

In one embodiment, the substrate is about 20 - 300 μ m thick. In some cases, the thin substrate may be mechanically unstable, creating processing problems. A temporary support layer (not shown) can be employed to stabilize the substrate during the fabrication process. In one embodiment, the substrate is prepared with OLED cells. The temporary support layers, for example, can be provided on the backside of the substrate. In one embodiment, the temporary support layer comprises a polymer foil coated with an adhesive. After processing, the temporary layers are removed since the device

package can be used to mechanically stabilize the device.

The OLED cells are formed in the cell region 508 of the substrate. The OLED cells comprise a stack of
5 layers which includes first and second electrodes 505 and 515 with at least one organic functional layer 510 therebetween. Fabrication of OLED cells is described in, for example, Burroughes et al., Nature (London) 347, 539 (1990) which is herein incorporated by reference for
10 all purposes. Bond pads 550 are provided to provide access to the OLED cells.

Typically, the fabrication of the OLED cells includes depositing a first electrode layer 505 on the substrate. The first electrode layer comprises, for
15 example, a transparent conductive layer such as indium-tin-oxide. Other transparent conductive materials are also useful. The first electrode layer is patterned using conventional etch and mask techniques. Patterning techniques using a stamp are also useful. Such
20 techniques are described in co-pending international patent application titled "Mechanical Patterning of a Device Layer" (attorney docket number 99E 8062), which is herein incorporated by reference for all purposes. The pattern of the electrode layer depends on the

application. For example, the first electrode layer can be patterned to form lower electrode strips which serve as anodes of a pixelated device. Connections to bond pads can also be formed.

5 One or more organic functional layers 510 are formed on the substrate, covering the first electrodes. The functional organic layers comprise, for example, conjugated polymer or low molecular materials such as Alq₃. Other types of functional organic layers are also
10 useful. The organic functional layers can be formed by conventional techniques. Such techniques include, for example, wet processes such as spin coating or vacuum sublimation (for Alq₃ organic layers). Portions of the organic functional layers can be selectively removed to
15 expose, for example, the bond pad connections. Selective removal of the organic layers can be achieved using, for example, polishing. Other techniques for selective removal of the organic layers, such as etching, scratching, or laser ablation, can also be
20 used.

A second electrode layer 515 is deposited on the substrate over the organic functional layers and patterned. In one embodiment, the second electrode layer is patterned to form second electrode strips which

intersect with the first electrode strips to create a
pixelated OLED device. Spatially depositing the
conductive layer to form second electrodes is also
useful. Other techniques for forming OLED cells are
5 also useful.

In one embodiment, a sealing frame 535 surrounding
the cell region is prepared. Preparation of the sealing
frame includes patterning the substrate, if necessary,
to form an area for forming a sealing post 580 therein.
10 The height of the sealing post is sufficient to form a
cavity with the desired height.

The sealing post, for example, comprises an
adhesive for permanently sealing the cap to the device.
Adhesives such as uv or thermal curable epoxy,
15 acrylates, hot melt adhesives, or low melt inorganic
materials (e.g., solder glass) can be used. Spacer
particles (not shown) can be provided in the sealing
post. The spacer particles can be provided in the
sealing post to support the cap during the uncured phase
20 of the adhesive in the sealing post. The sealing post
is formed using conventional techniques which include,
for example, screen-printing, dispensing, or
photolithography.

The sealing posts can be formed with other types of materials such as those described in co-pending international patent application titled "Encapsulation Of A Device" (attorney docket number 99E 1975), which is
5 herein incorporated by reference for all purposes. Preferably, the sealing post comprises a photopatternable or photosensitive material that can be directly patterned. Photopatternable materials include, for example, photosensitive polyimide, photosensitive
10 polybenzoxazole, photoresists, photoresists based on novolac systems, and dry film resist materials. Photoresists based on novolac systems are particularly useful since they can be cured and crosslinked to provide improved mechanical integrity over other types
15 of non-curable resists. Indirectly patternable materials, for example, spin-on glass materials, polyimide, polybenzoxazole, polyglutarimide, benzocyclobutene, polymers such as polyethylene (PE), polystyrene (PS), polypropylene (PP), inorganic
20 materials such as SiO_2 , Si_3N_4 , and Al_2O_3 , or ceramics, are also useful.

In such applications, adhesives can be provided on the sealing posts to seal the cap. Adhesives such as self-hardening adhesives, UV or thermal curable

adhesives, or hot melt adhesives are useful. Other techniques, such as those employing low temperature solder materials, ultrasonic bonding, or welding techniques using inductance or laser welding, are also
5 useful.

In alternative embodiments, the sealing post is formed on the inner surface of the cap instead of on the sealing frame.

Referring to Fig. 6, the cap is mounted on the
10 substrate. The sealing post seals the cap, encapsulating the device. The cap forms a cavity 645, supported by spacer particles 270. The spacer particles provide support for the cap to prevent it from contacting the OLED cells in the cell region, enabling
15 the use of thinner substrate and cap.

Figs. 7-8 show another embodiment of the invention. A cap 760 preformed with a cavity 745 is shown. The inner surface of the cap comprises spacer particles 770. The spacer particles comprise a
20 distribution pattern created by a directly or an indirectly patternable process in accordance with the invention. In embodiment, the spacer particle distribution corresponds to the non-active areas of the OLED device.

Referring to Fig. 8, a substrate 801 is prepared with OLED cells in the cell region 808. The OLED cells comprises at least one functional organic layer 810 sandwiched between first and second electrodes 805 and 815. The electrodes can be patterned to form a pixelated OLED device. Bond pads 850 are provided to access the OLED cells. The cap 760 with the spacer particles is mounted in the cap sealing frame region 835 of the substrate to encapsulate the device. Adhesives such as self-hardening adhesives, UV or thermal curable adhesives, or hot melt adhesives are useful to mount the cap. Other techniques, such as those employing low temperature solder materials, ultrasonic bonding, or welding techniques using inductance or laser welding, are also useful for mounting the cap. The spacer particles provide support for the cap to prevent it from contacting the OLED cells in the cell region, enabling the use of thinner substrate and cap.

Figs. 9-13 show a process for forming OLED device in accordance with another embodiment of the invention. As shown, a substrate 901 is provided. The substrate serves as a support for OLED cells. The substrate comprises, for example, glass or polymer. Other materials are also useful. A conductive layer 905 is

formed on the surface of the substrate. The conductive layer serves as an electrode for OLED cells. In one embodiment, the first electrode layer comprises, for example, a transparent conductive layer such as ITO.

5 Other transparent conductive materials are also useful.

The conductive layer is patterned as desired to selectively remove portions of the layer, exposing portions 956 of the substrate. In one embodiment, the conductive layer is patterned to form strips that serve

10 as, for example, the anode of a pixelated OLED device. The patterning process can also form connections for bond pads. Conventional techniques, such as photolithography and etching, can be used to pattern the conductive layer. Patterning techniques using a stamp

15 are also useful. Such techniques are described in co-pending international patent application titled "Mechanical Patterning of a Device Layer" (attorney docket number 99E 8062), which is herein incorporated by reference for all purposes.

20 Referring to Fig. 10, a resist layer 966 with spacer particles suspended therein is deposited on the substrate in accordance with one embodiment of the invention. Radiation 998 from an exposure source is

directed through a mask 995 to selectively expose portions 997 of the resist.

In Fig. 11, the resist layer 966 is developed removing the exposed or unexposed portions, depending on whether a positive or negative resist is used. The spacer particles are removed along with the resist layer, creating a spacer particle distribution pattern. Alternatively, the spacer particle distribution pattern can be formed by indirectly patternable processes in accordance with the invention.

One or more functional organic layers 910 are deposited on the substrate, covering the exposed substrate portions and the conductive layer. The functional organic layers comprise, for example, conjugated polymer or Alq_3 . Other types of functional organic layers are also useful.

Referring to Fig. 12, portions of the organic layers can be selectively removed, for example, to expose underlying layer for bond pad connections. Selective removal of the organic layers can be achieved using polishing. Other techniques, such as etching, scratching, or laser ablation, can also be used to selectively remove portions of the organic layers.

A second conductive layer 915 is deposited on the substrate, covering the spacer particles and other layers formed thereon. The conductive layer comprises, for example, Ca, Mg, Ba, Ag, Al or a mixture or alloy thereof. Other conductive materials, particularly those which comprises a low work function, can also be used to form the second conductive layer. In one embodiment, the second conductive layer is patterned to form electrode strips that serve as cathode for a pixelated OLED device. Also, connections for bond pads can be formed during the patterning process. Alternatively, the conductive layer can be selectively deposited to form cathode strips and bond pad connections. Selective deposition of the conductive layer can be achieved with, for example, mask layers. The cathode strips are typically orthogonal to the anode strips. Forming cathode strips that are diagonal to the anode strips is also useful. The intersections of the top and bottom electrode strips form organic LED pixels.

A sealing post 980 is formed in a sealing frame region 935. The sealing post serves to support and permanently seal the cap onto the device. Forming the sealing post on the inner surface of the cap instead of on the sealing frame is also useful.

Referring to Fig. 13, the cap 960 is mounted on the substrate. The sealing post seals the cap, encapsulating the device. The cap forms a cavity 945, supported by spacer particles 270. The spacer particles
5 provide support for the cap to prevent it from contacting the OLED cells in the cell region, enabling the use of thinner substrate and cap.

Referring to Fig. 14, an alternative embodiment of the invention is shown. A cap 960 preformed with a
10 cavity 945 is shown. The cap is mounted in a sealing frame region of the substrate prepared with active components and spacer particles to encapsulate the device. Adhesives such as self-hardening adhesives, UV or thermal curable adhesives, or hot melt adhesives are
15 useful to mount the cap. Other techniques, such as those employing low temperature solder materials, ultrasonic bonding, or welding techniques using inductance or laser welding, are also useful for mounting the cap. The spacer particles provide support
20 for the cap to prevent it from contacting the OLED cells in the cell region, enabling the use of thinner substrate and cap.

Spacer particles can also be useful in providing support in other types of devices that employ cavity

packages. Such devices include, for example, electrical devices, mechanical devices, electromechanical devices, or microelectromechanical systems (MEMS).

While the invention has been particularly shown and
5 described with reference to various embodiments, it will be recognized by those skilled in the art that modifications and changes may be made to the present invention without departing from the spirit and scope thereof. The scope of the invention should therefore be
10 determined not with reference to the above description but with reference to the appended claims along with their full scope of equivalents.

What is claimed is:

1. A device comprising:
 - a substrate with a device region;
 - a cap for encapsulating the device, the cap creates
 - 5 a cavity over the device region; and
 - spacer particles selectively located in the device region to support the cap.
2. The device of claim 1 wherein the device region
- 10 comprises OLED cells.
3. The device of claim 2 wherein the spacer particles
- are selectively located in non-active areas of the device region.
- 15
4. The device of claim 3 wherein the spacer particles comprise a non-conductive material.
5. The device of claim 1 wherein the spacer particles
- 20 are selectively located in non-active areas of the device region.
6. The device of claim 5 wherein the spacer particles comprise a non-conductive material.

7. The device of claim 1, 2, 3, 4, 5, or 6 comprises a device layer used for depositing the spacer particles in the device region.

5

8. The device of claim 7 wherein the device layer comprises a photosensitive material

9. The device of claim 8 wherein the spacer particles
10 comprises a mean diameter to maintain a height of the cavity between the cap and substrate.

10. The device of claim 7 wherein the device layer comprises a non-photosensitive material

15

11. The device of claim 10 wherein the spacer particles comprises a mean diameter to maintain a height of the cavity between the cap and substrate.

20 12. A method for forming a device comprises:
providing a substrate with a device region;
providing a cap having spacer particles selectively located on an inner surface of the cap; and

mounting a cap on the substrate to encapsulate the device, the cap forms a cavity in the device region supported by spacer particles.

- 5 13. The method of claim 12 wherein the device comprises an OLED device with OLED cells in the device region.

14. The method of claim 13 wherein the spacer particles comprise a non-conductive material.

10

15. The method of claim 12 wherein the spacer particles comprise a non-conductive material.

16. The method of claim 12, 13, 14, or 15 wherein
15 providing the cap with spacer particles comprises:
suspending spacer particles in a material;
coating the material with spacer particles on the
inner surface of the cap; and
selectively removing the material with the spacer
20 particles from the inner surface.

17. The method of claim 16 wherein the material comprises a photosensitive material.

18. The method of claim 17 wherein selectively removing the spacer particles comprises:

exposing the photosensitive material with an exposure source through a pattern mask; and

5 developing the photosensitive material to selectively remove portions of the photosensitive material along with the spacer particles thereon.

19. The method of claim 18 wherein the spacer particles
10 comprise a mean diameter maintain a height of the cavity between the cap and substrate.

20. The method of claim 19 wherein the spacer particles are deposited on the substrate with a density to
15 maintain the cavity.

21. The method of claim 18 wherein the spacer particles are deposited on the substrate with a density to maintain the cavity.

20

22. The method of claim 16 wherein the material comprises a non-photosensitive material.

23. The method of claim 22 wherein selectively removing the spacer particles comprises:

depositing a photosensitive material on the inner surface of the substrate, covering the material with the
5 spacer particles;

exposing the photosensitive material with an exposure source through a pattern mask; and

developing the photosensitive material to selectively remove portions of the photosensitive
10 material, leaving remaining portions;

removing the material and spacer particles unprotected by the remaining portions of the
photosensitive material; and

removing the remaining portions of the photoresist,
15 leaving spacer particles selectively located on the inner surface.

24. The method of claim 23 wherein the spacer particles comprise a mean diameter maintain a height of the cavity
20 between the cap and substrate.

25. The method of claim 24 wherein the spacer particles are deposited on the substrate with a density to maintain the cavity.

26. The method of claim 24 wherein the spacer particles are deposited on the substrate with a density to maintain the cavity.

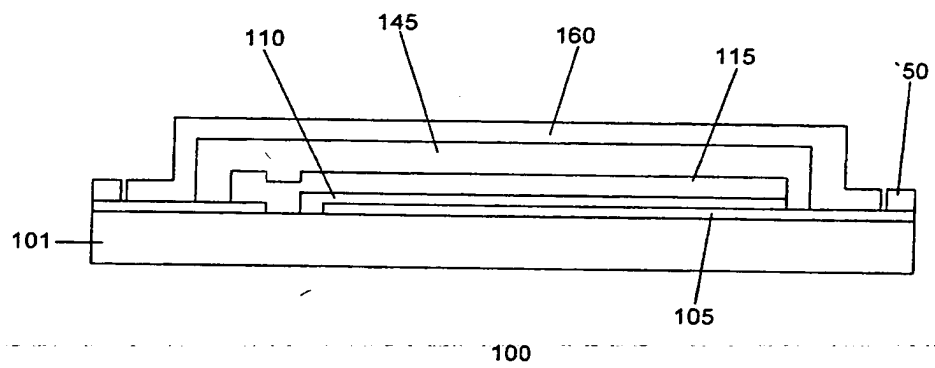


Fig. 1

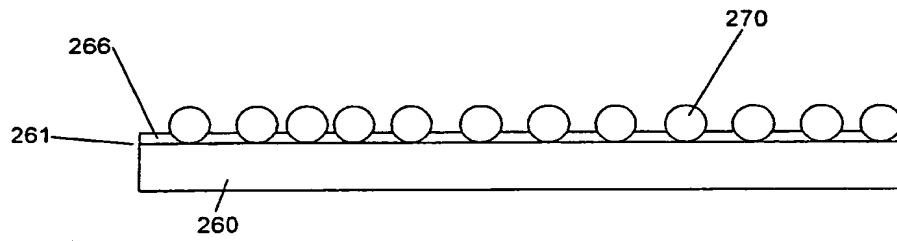


Fig. 2

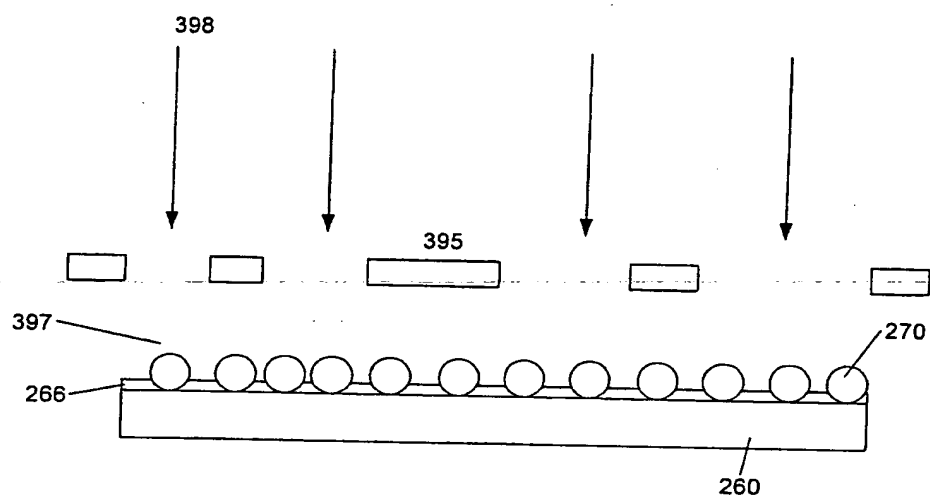


Fig. 3

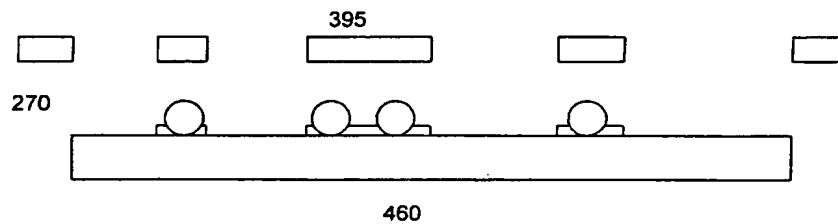


Fig. 4

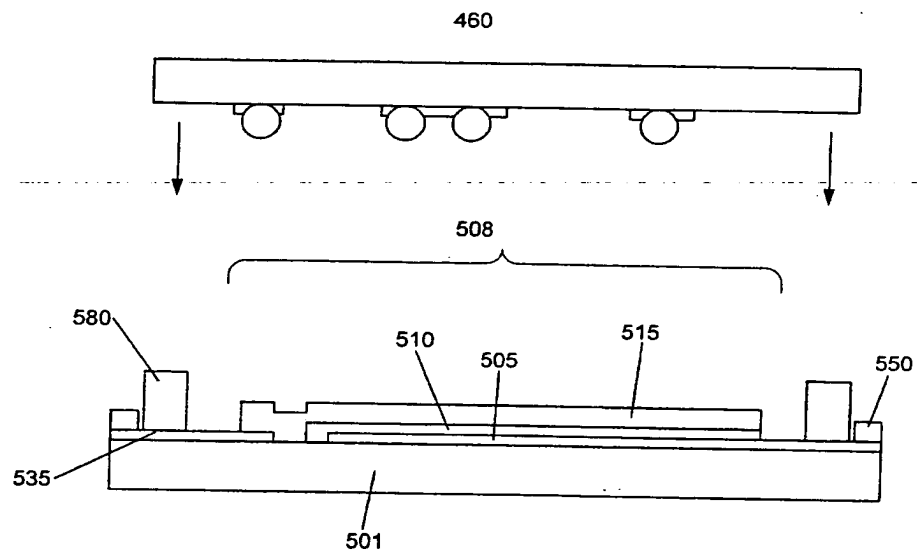


Fig. 5

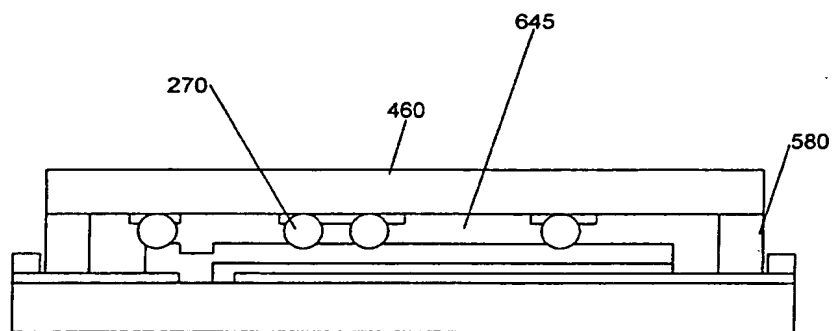


Fig. 6

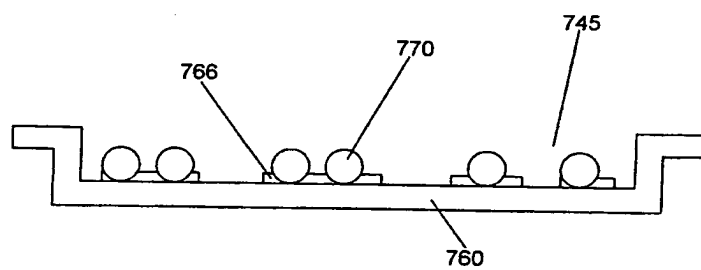


Fig. 7

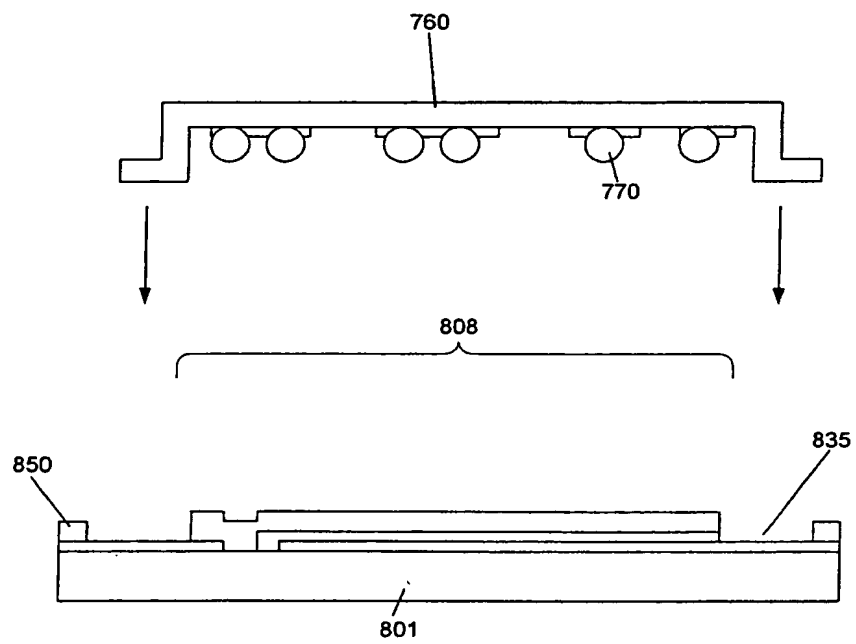


Fig. 8

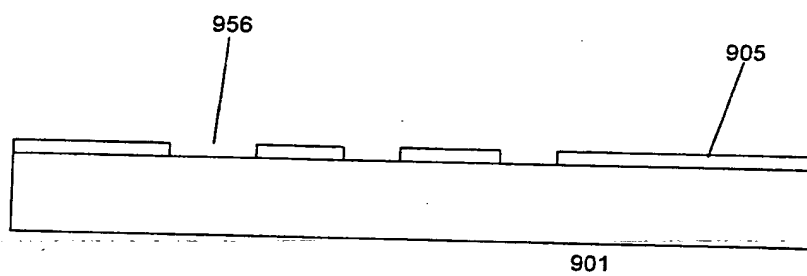


Fig. 9

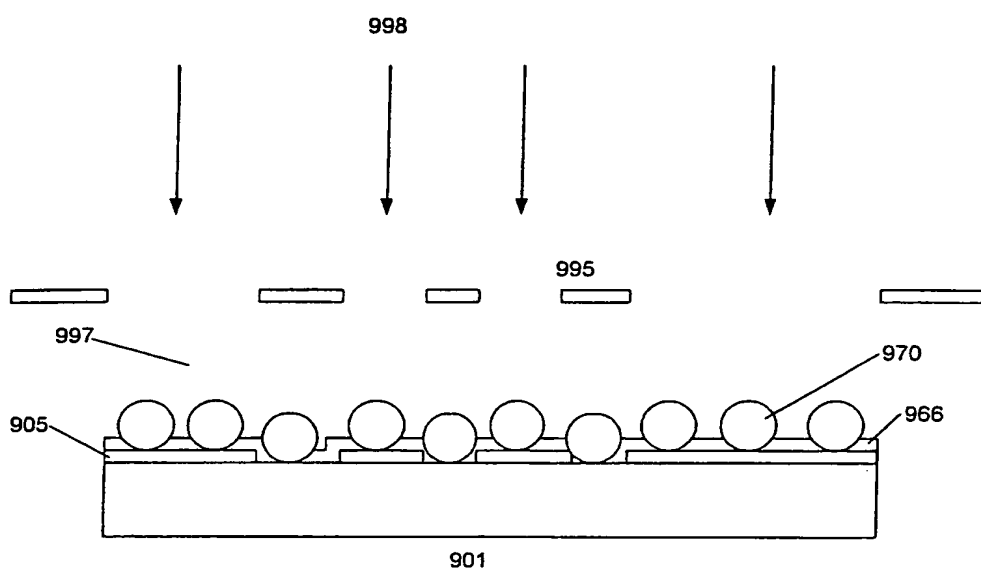


Fig. 10

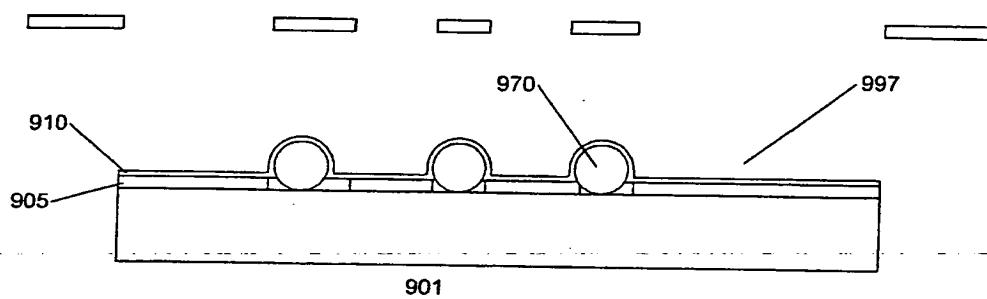


Fig. 11

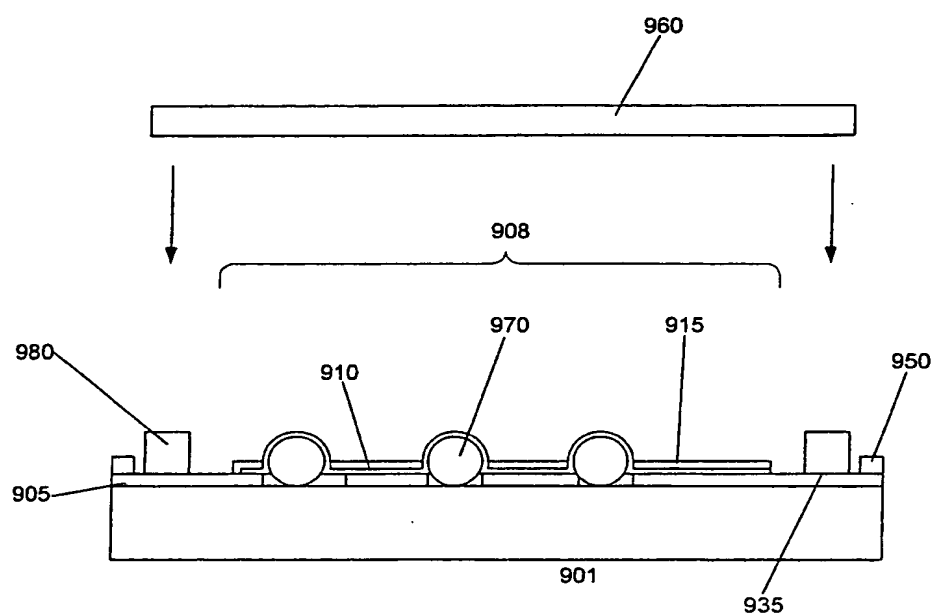


Fig. 12

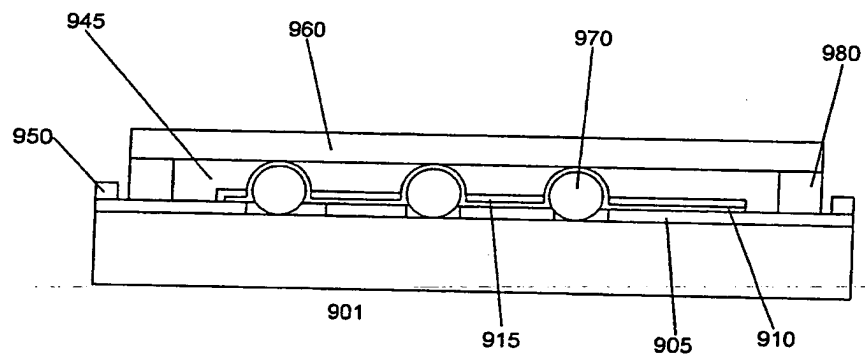


Fig. 13

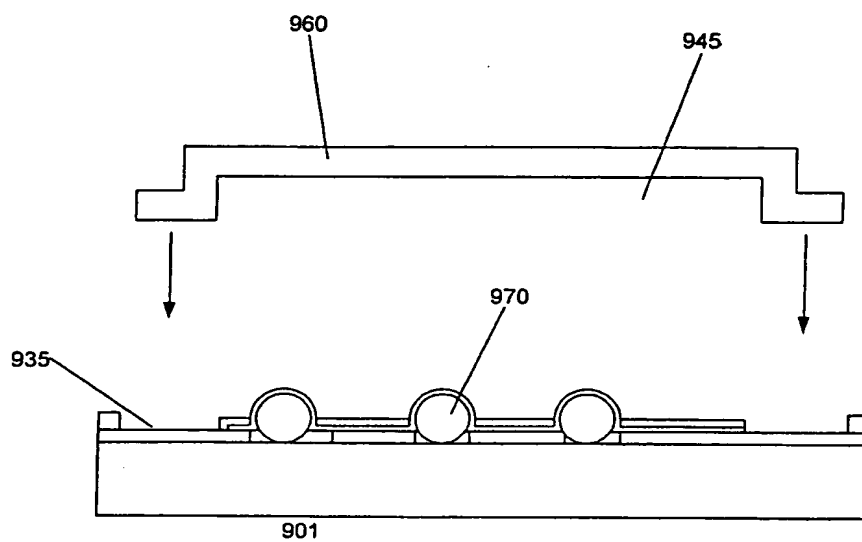


Fig. 14

INTERNATIONAL SEARCH REPORT

International application No.
PCT/SG 99/00145

CLASSIFICATION OF SUBJECT MATTER

IPC⁷: G02F 1/1339

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁷: G02F, H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI, PAJ, EPODOC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 58 160927 A (SEIKOKOUSHIYA:KK) 24 September 1983 (24.09.83) (abstract). [online] [retrieved on 2000-09-05]. Retrieved from: EPO PAJ Database.	1-26
X	JP 04 90512 A (TOKUYAMA SOKA KK) 24 March 1992 (24.03.92) (abstract). [online] [retrieved on 2000-09-05]. Retrieved from: EPO PAJ Database.	1-26
X	JP 04 136916 A (SEIKOSHA CO LTD) 11 May 1992 (11.05.92) (abstract). [online] [retrieved on 2000-09-05] Retrieved from: EPO PAJ Database.	1-15

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

Special categories of cited documents:

„A“ document defining the general state of the art which is not considered to be of particular relevance

„E“ earlier application or patent but published on or after the international filing date

„L“ document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

„O“ document referring to an oral disclosure, use, exhibition or other means

„P“ document published prior to the international filing date but later than the priority date claimed

„T“ later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

„X“ document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

„Y“ document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

„&“ document member of the same patent family

Date of the actual completion of the international search

5 September 2000 (05.09.2000)

Date of mailing of the international search report

12 March 2001 (12.03.2001)

Name and mailing address of the ISA/AT

Austrian Patent Office
Kohlmarkt 8-10; A-1014 Vienna

Facsimile No. 1/53424/535

Authorized officer

MESA PASCASIO

Telephone No. 1/53424/327

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.
PCT/SG 99/00145

Patent document cited in search report			Publication date	Patent family member(s)	Publication date
JP	A2	4136916	11-05-1992	none	
JP	A2	4090512	24-03-1992	none	
JP	A2	58160927	24-09-1983	none	

THIS PAGE BLANK (USPTO)